Listing of Claims:

1. (Currently Amended) A radio wave receiver ~~for receiving radio waves having a predetermined frequency, the receiver~~ comprising:

an antenna;

[[an]] a variable capacitor section connected to the antenna, wherein the variable capacitor section comprises capacitors and switching elements that are connected to the capacitors in series;

a memory which is configured to store at least two items of data for setting a capacitance of the variable capacitor section to at least two suitable values that are suitable for receiving radio waves having at least two frequencies; and

a controller which (i) detects suitable combinations of on and off states of the switching elements such that ~~determines an optimum capacitance of the variable capacitor with which~~ the radio wave receiver is in a predetermined reception state for the at least two frequencies, ~~and~~ (ii) writes ~~optimum capacitance data~~ into the memory ~~and, controls the variable capacitor based on the optimum capacitance~~ data for setting the on and off states of the switching elements to the suitable combinations, (iii) reads the data from the memory in accordance with a

received radio wave, and (iv) turns on and off the switching elements based on the read data.

Claim 2 (Canceled).

3. (Currently Amended) The radio wave receiver according to claim 1, wherein the ~~capacitors comprise~~ variable capacitor section comprises internal capacitors provided in a capacitor module and external capacitors provided outside the capacitor module.

4. (Currently Amended) The radio wave receiver according to claim 1, further comprising a reception state detector which detects a reception state, ~~and~~

wherein the controller controls the ~~variable capacitor~~ switching elements in such a manner that a capacitance component connected to the antenna gradually varies when receiving the radio ~~waves~~ wave having ~~the predetermined frequency~~ one of the at least two frequencies, and writes ~~optimum capacitance data~~ into the memory data indicating a present combination of on and off states of the switching elements when the reception state detector detects the predetermined reception state.

5. (Currently Amended) The radio wave receiver according to claim 4, wherein the controller controls the switching elements in such a manner that the ~~varies a capacitance of the variable capacitor in a direction along which a~~ capacitance component ~~to be~~ connected to the antenna is increased, and wherein the controller writes ~~optimum capacitance data~~ into the memory data indicating the present combination of on and off states of the switching elements ~~, the optimum capacitance data for setting the capacitance of the variable capacitor to a capacitance~~ immediately before a change of a reception level shifts from an increase to a decrease.

Claims 6 and 7 (Canceled).

8. (Currently Amended) The radio wave receiver according to claim 1, wherein the radio waves ~~having the predetermined frequency~~ which have the at least two frequencies comprise a standard time signal including a time code.

9. (Currently Amended) The radio wave receiver according to claim 1, ~~having~~ wherein the radio wave receiver has a receiving mode and a tuning mode,

wherein the controller writes into the memory ~~optimum capacitance~~ data for setting the capacitance of the variable

capacitor section to one of the at least two suitable values such that the radio wave receiver is in the predetermined reception state for receiving the radio wave which has one of the at least two frequencies in the tuning mode, and

wherein the controller turns on and off the switching elements based on the data in the memory to set the ~~sets a~~ capacitance of the variable capacitor section to the ~~optimum capacitance~~ one of the at least two suitable values in the receiving mode.

Claim 10 (Canceled).

11. (Currently Amended) A radio-controlled timepiece comprising:

a radio wave receiver ~~for receiving~~ which receives a radio ~~waves having a predetermined frequency,~~ wave;

a time code generator configured to generate a time code based on the radio wave received by the radio wave receiver;

a clocking unit which counts a current time; and

a correction unit which corrects current time counted by the clocking unit based on the time code generated by the time code generator;

wherein the radio wave receiver comprises ~~comprising~~:

an antenna;

~~an~~ a variable capacitor section connected to the antenna, wherein the variable capacitor section comprises capacitors and switching elements connected to the capacitors in series;

a memory which is configured to store at least two items of data for setting a capacitance of the variable capacitor section to at least two suitable values that are suitable for receiving radio waves having at least two frequencies; and

a controller which: (i) detects suitable combinations of on and off states of the switching elements such that ~~determines an optimum capacitance of the variable capacitor with which~~ the radio wave receiver is in a predetermined reception state for each of the at least two frequencies, ~~and~~ (ii) writes ~~optimum capacitance data~~ into the memory data for setting the on and off states of the switching elements to the suitable combinations, (iii) reads the data from the memory in accordance with the received radio wave, and (iv) turns on and off the switching elements based on the read data. ~~and, controls the variable capacitor based on the optimum capacitance data;~~

~~a time code generator which generates a time code based on the radio waves received by the radio wave receiver;~~

~~a clocking unit which counts a current time; and~~

~~a correction unit which corrects current time counted by the clocking unit based on the time code generated by the time code generator.~~

Claim 12 (Canceled).

13. (Currently Amended) The radio-controlled timepiece according to claim ~~12~~ 11, wherein the ~~capacitors comprise~~ variable capacitor section comprises internal capacitors provided in a capacitor module and external capacitors provided outside the capacitor module.

14. (Currently Amended) The radio-controlled timepiece according to claim 11, further comprising a reception state detector which detects a reception state, ~~and~~

wherein the controller controls the ~~variable capacitor~~ switching elements in such a manner that a capacitance component connected to the antenna gradually varies when receiving the radio ~~waves~~ wave having ~~the predetermined frequency~~ one of the at least two frequencies, and writes ~~optimum capacitance data~~ into the memory data indicating a present combination of on and off states of the switching elements when the reception state detector detects the predetermined reception state.

15. (Currently Amended) The radio-controlled timepiece according to claim 14, wherein the controller controls the switching elements in such a manner that the ~~varies a capacitance of the variable capacitor in a direction along which a~~ capacitance component ~~to be~~ connected to the antenna is increased, and wherein the controller writes ~~optimum capacitance data~~ into the memory data indicating the present combination of on and off states of the switching elements ~~, the optimum capacitance data for setting the capacitance of the variable capacitor to a capacitance~~ immediately before a change of a reception level shifts from an increase to a decrease.

Claims 16 and 17 (Canceled).

18. (Currently Amended) The radio-controlled timepiece according to claim 11,

wherein the radio wave receiver ~~having~~ has a receiving mode and a tuning mode, and

wherein the controller writes into the memory ~~optimum capacitance~~ data for setting the capacitance of the variable capacitor section to one of the at least two suitable values such that the radio wave receiver is in the predetermined reception state for receiving the radio wave which has one of the at least two frequencies in the tuning mode, and

wherein the controller turns on and off the switching elements based on the data in the memory to set the ~~sets a~~ capacitance of the variable capacitor section to the ~~optimum capacitance~~ one of the at least two suitable values in the receiving mode.

Claims 19-22 (Canceled).